

**ELECTRONICALLY FILED ON 29 JUNE 2006**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of inventor(s):  
Albert Sun et al.

Application No. 10/699,764

Filing Date: 03 November 2003

Title: In-Circuit Configuration Architecture  
for Embedded Configurable Logic  
Array

Confirmation No. 4234

Group Art Unit: 2186

Examiner: Chase W. Peers

**CUSTOMER NO. 22470**

**MAIL STOP AF**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**AMENDMENT AFTER FINAL  
UNDER 37 CFR § 1.116 -- EXPEDITED PROCEDURE**

Sir:

In response to the office action mailed 01 May 2006, please amend this application as follows.

**AMENDMENTS TO THE CLAIMS** are reflected in the listing of claims which begins on page 2 of this paper. This listing of the claims will replace all prior versions, and listings, of the claims in this application.

**REMARKS** begin on page 7 of this paper.